

DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Methods of Forming Contacts, Methods of Contacting Lines, Methods of Operating Integrated Circuitry, and Integrated Circuits, Serial No. 09/146,115, filed September 2, 1998.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful

1 false statement may jeopardize the validity of the application or any
2 patent issued therefrom.

3 * * * * *

4 Full name of inventor: Luan C. Tran

5 Inventor's Signature: Luan C. Tran

6 Date: 1-18-1999.

7 Residence: Meridian, Idaho

8 Citizenship: U.S.A.

9 Post Office Address: 1125 W. Sandy Ct.
Meridian, ID 83642

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I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Methods of Forming Contacts, Methods of Contacting Lines, Methods of Forming Dynamic Random Access Memory Circuitry, Methods of Operating Integrated Circuitry, Integrated Circuits, Dynamic Random Access Memory Circuitry, the specification of which is attached hereto.

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Section 1001 of Title 18 of the United States Code and that such willful
false statement may jeopardize the validity of the application or any
patent issued therefrom.

* * * * *

Full name of inventor: Robert Kerr

Inventor's Signature: *Robert Kerr*

Date: 8-28-98

Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 3089 E. Bonview Drive
Boise, ID 83712

* * * * *

Full name of inventor: Brian Shirley

Inventor's Signature: *Brian Shirley*

Date: 8/31/98

Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 2484 Sunshine Drive
Boise, ID 83712

* * * * *

Full name of inventor: Luan C. Tran

Inventor's Signature: _____

Date: _____

Residence: Meridian, Idaho

Citizenship: U.S.A.

Post Office Address: 1125 W. Sandy Ct.
Meridian, ID 83642

* * * * *

Full name of inventor: Tyler A. Lowrey

Inventor's Signature: _____

Date: _____

Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 2599 E. Plateau Drive
Boise, ID 83712

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* * * * *

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Inventor's Signature: _____

Date: _____

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* * * * *

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Inventor's Signature: _____

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Boise, ID 83712**

* * * * *

Full name of inventor: Luan C. Tran

Inventor's Signature: _____

Date: _____

Residence: Meridian, Idaho

Citizenship: U.S.A.

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Meridian, ID 83642

* * * * *

Full name of inventor: Tyler A. Lowrey

Inventor's Signature: Tyler A. Lowrey

Date: 8/27/98

Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 2599 E. Plateau Drive
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